

## EAST Search History

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|-------|------|--------------------------------|---|------------------|---------|------------------|
| L4    | 2059 | (714/6).ccls.                  | US-PGPUB;<br>USPAT;<br>USOCR                            | OR               | ON      | 2006/12/28 14:28 |
| L5    | 70   | hold with request with failure | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/12/28 14:29 |
| L6    | 14   | L5 and "714"                   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/12/28 14:29 |
| L7    | 4    | L5 and (714/6)"ccls."          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/12/28 14:29 |
| L8    | 70   | hold with request with failure | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2006/12/28 14:29 |

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switch failover

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Furthermore, the failover switch time is quick enough that.  
it would not affect the upperlayer applications. With 4 or.  
more bundling channels, it would ...  
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switch (APS) controlled by the fail over mechanism ensures that  
when the DP. has switched, the corresponding I/O is also  
switched. ...  
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Furthermore, the failover switch time is so rapid that. the upper-layer applications are  
not affected. With 4. or more trunking channels, ELT performance is ...  
www.springerlink.com/index/FT15442011448R41.pdf  
[ [More results from www.springerlink.com](#) ]**Reviews**Four switch-module bays are in the rear of the chassis; the second and ... to allow  
these interfaces to be used individually or in a failover fashion. ...  
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**failover**/[{a,b}.failover](#)}. Note that the **failover** action causes an atomic **switch** from master to slave, as in the specification. Despite this, the client ...  
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type=pdf&coll=&dl=ACM&CFID=15151515&CFTOKEN=...  
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**Switch** customers, we capture raw **switch** output and transmit it to a central process-  
... Contributing to the reliability are the **failover**- ...  
doi.wiley.com/10.1002/bltj.2219

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**Failover** rate, or the rate at which the system can. **switch** from a failed unit to the spare unit. • Initialization rate, the rate at which the system ...  
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minimal downtime and automatic **failover** in the event of a processor failure. ... A **switch** upgrade may be necessary to utilize the new interfaces. ...  
www.iop.org/EJ/article/1742-6596/46/1/071/jpconf6\_46\_071.pdf

[Blackwell Synergy: Dig Endosc, Vol 14, Issue S1, pp. S10-S12 ...](#)

... then the papillotome can be inserted without **fail over** the guide-wire. ... speed of the incision wave by means of pressure against a **switch** pedal. ...  
www.blackwell-synergy.com/doi/abs/10.1046/j.1443-1661.14.s1.18.x

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